

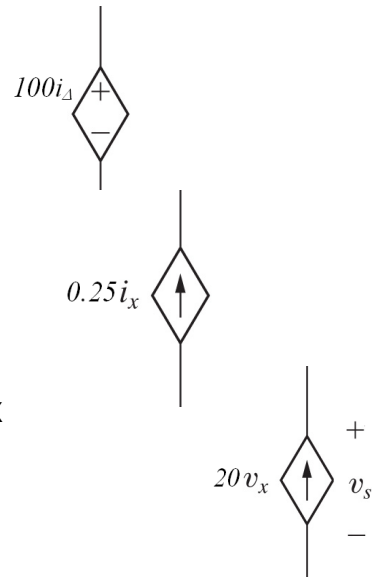
EECS2200 Electric Circuits

Chapter 2

Activities

Activity 1

- If i_{Δ} is 15 mA, how much voltage does this dependent source supply?
- What units are associated with the gain 0.25 for this dependent source?
- Suppose the value of v_x is 4 V. What is the value of v_s ?

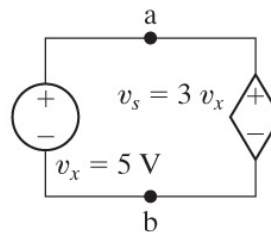
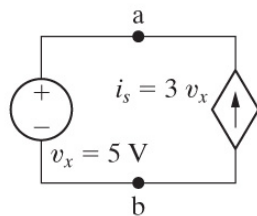


Solution

- (a) $v = 100 \text{ A/V} * 15 \text{ mA} = 1.5 \text{ V}$.
- (b) Since this is the dependent current source, the output current $i = 0.25 * i_x$.
The unit of gain is A/A .
- (c) This is a current source, we can't calculate the voltage v_s based on given information.

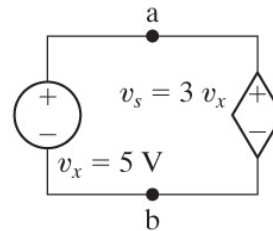
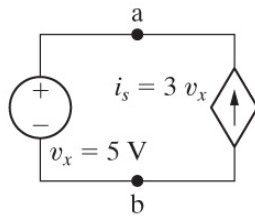
Activity 2

- Which of the circuits below is valid?



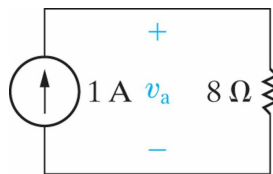
Solution

The circuit on the left is valid.

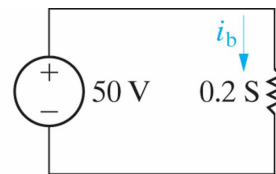


Activity 3

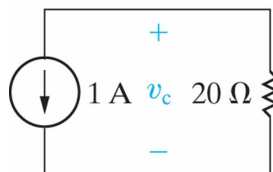
find v or i in every circuit



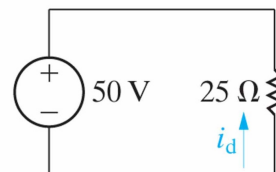
(a)



(b)



(c)



(d)

Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

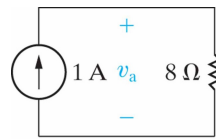
Solution

(a) $v_a = iR = 1 \cdot 8 = 8V$

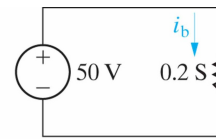
(b) $i_b = V/R = VG = 50 \cdot 0.2 = 10A$

(c) $v_c = iR = -1 \cdot 20 = -20V$

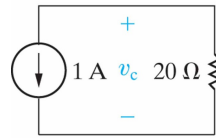
(d) $i_d = V/R = -50/25 = -2A$



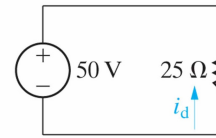
(a)



(b)



(c)

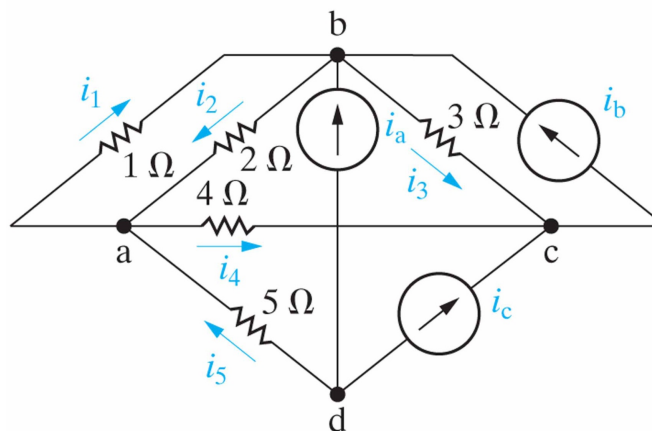


(d)

Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

Activity 4

Sum the currents at each node in the circuit.



Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

Solution

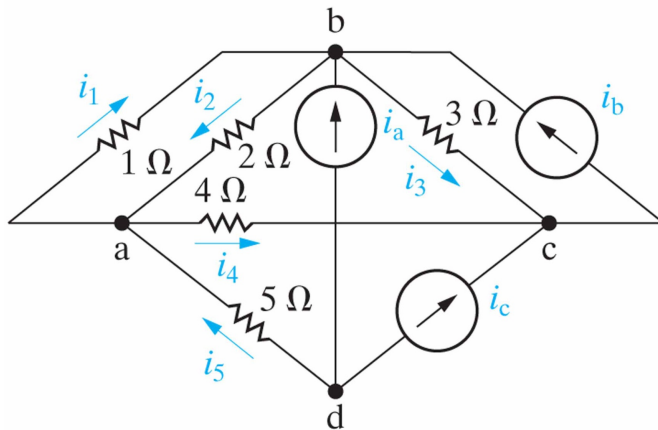
a: $-i_2 - i_5 + i_1 + i_4 = 0$

b: $i_2 + i_3 - i_1 - i_a - i_b = 0$

c: $i_b - i_3 - i_c - i_4 = 0$

d: $i_5 + i_a + i_c = 0$

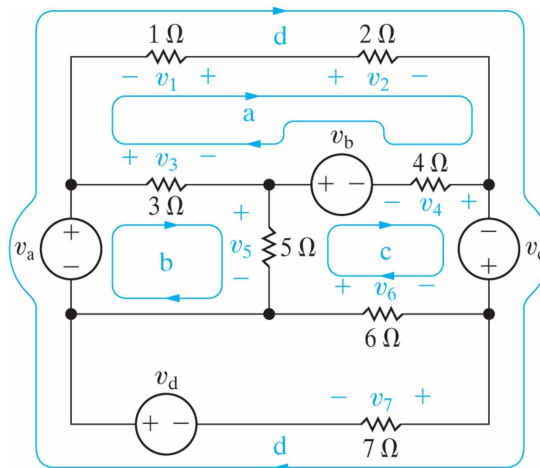
leaving = positive



Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

Activity 5

Sum the voltage around each designated path in the circuit.



Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

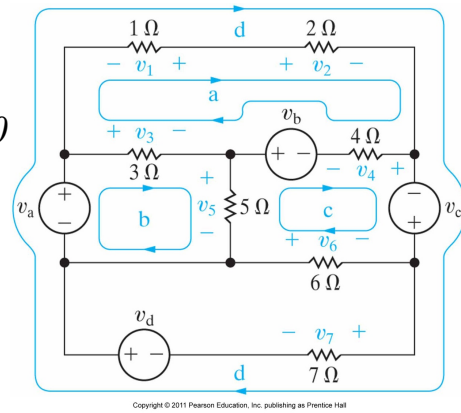
Solution

a: $-v_1 + v_2 + v_4 - v_b - v_3 = 0$

b: $v_3 + v_5 - v_a = 0$

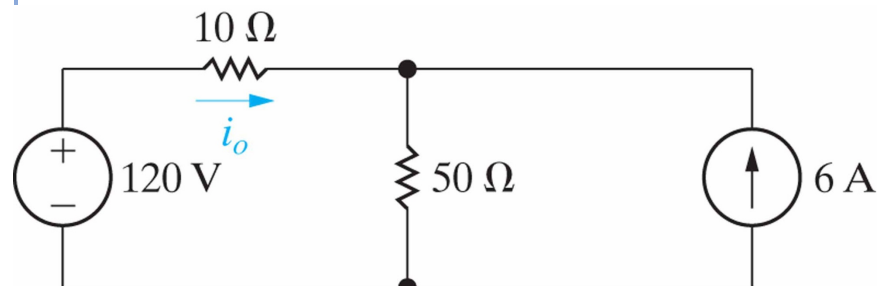
c: $v_b - v_4 - v_c - v_6 - v_5 = 0$

d: $-v_a - v_1 + v_2 - v_c + v_7 - v_d = 0$



Activity 6

Use Kirchhoff's Law and Ohm's Law to find i_o in the circuit.

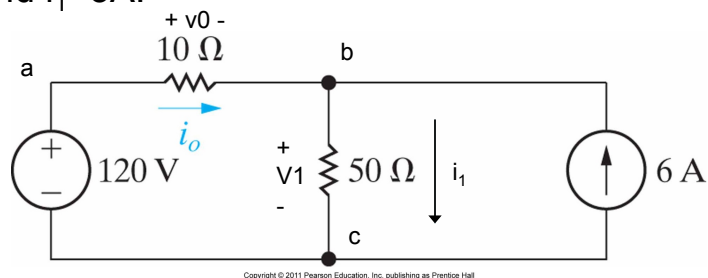


Solution

Applying KCL to node b: $i_1 - i_0 - 6 = 0 \rightarrow -i_0 + i_1 = 6$

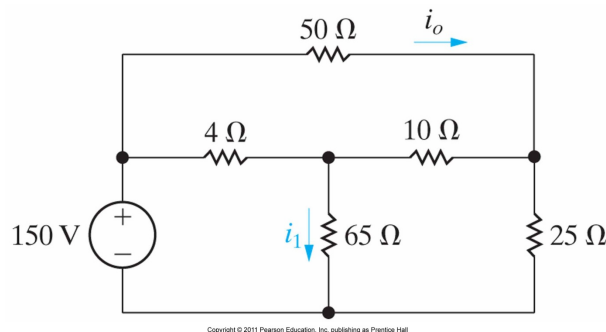
Applying KVL to loop "abca",
 $v_0 + v_1 - 120 = 0 \rightarrow 10i_0 + 50i_1 = 120$

Based on two equations, we can solve i_0 and i_1
 $i_0 = -3\text{A}$ and $i_1 = 3\text{A}$.



Activity 7

- (1) If $i_0 = 1\text{A}$, find i_1
- (2) Find the power dissipated in every resistor



Solution

(1) Given $i_0=1\text{A}$, we have:

$$v_2=150-50 \cdot 1=100\text{V (KVL, acda)}$$

$$i_2=v_2/25=4\text{A (Ohm's Law, OL)}$$

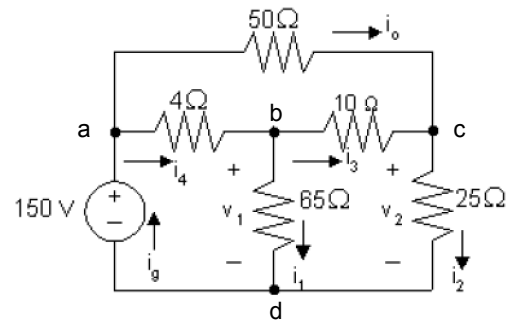
$$i_3+1=i_2, i_3=3\text{A (KCL, c)}$$

$$v_1=10 \cdot i_3+25 \cdot i_2=130\text{V (KVL, bcdb)}$$

$$i_1=v_1/65=2\text{A (OL)}$$

$$i_4=i_1+i_3=5\text{A (KCL, b)}$$

$$i_g=i_4+i_0=6\text{A (KCL, a)}$$



Solution

(2) $i_0=1\text{A}$, $i_1=2\text{A}$, $i_2=4\text{A}$, $i_3=3\text{A}$, $i_4=5\text{A}$

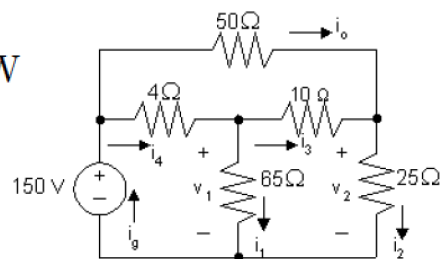
$$p_{4\Omega} = 5^2(4) = 100 \text{ W}$$

$$p_{50\Omega} = 1^2(50) = 50 \text{ W}$$

$$p_{65\Omega} = 2^2(65) = 260 \text{ W}$$

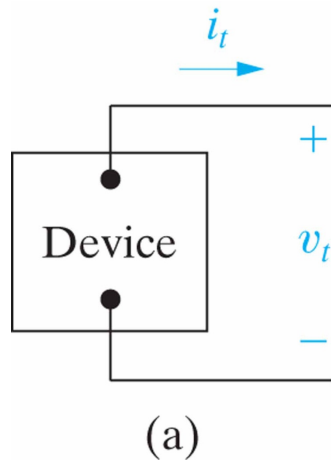
$$p_{10\Omega} = 3^2(10) = 90 \text{ W}$$

$$p_{25\Omega} = 4^2(25) = 400 \text{ W}$$



Activity 8

Construct a circuit model for a device



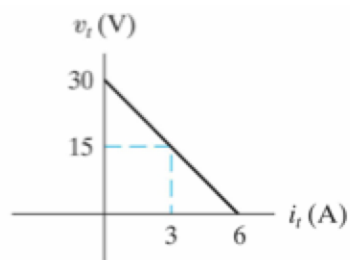
v_t (V)	i_t (A)
30	0
15	3
0	6

(b)

Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

Solution

Based on the V-I relation in Table, we can plot the voltage as a function of the current.

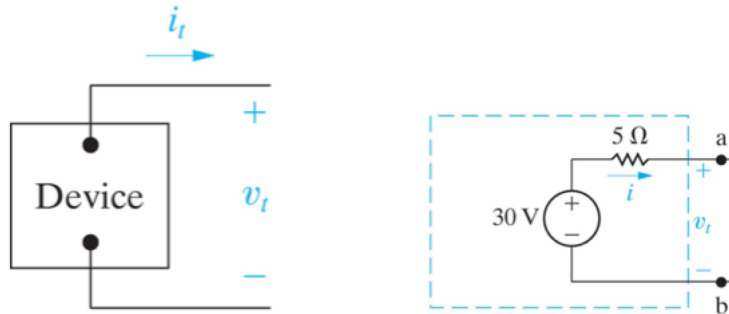


v_t (V)	i_t (A)
30	0
15	3
0	6

From the plot, we have: $v_t = 30 - 5i_t$

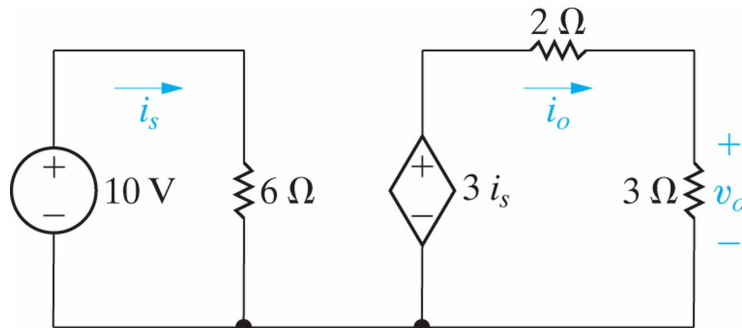
Solution

Given: $v_t = 30 - 5i_t \rightarrow$ a KVL equation in a loop
 $30 \rightarrow$ independent voltage source
 $5i_t \rightarrow$ a current passes through a 5Ω resistor



Activity 9

- (a) Use Kirchhoff's Law and Ohm's Law to find the voltage v_0 .
- (b) Show that the solution is consistent with the constraint that the total power developed in the circuit equals the total power dissipated.



Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

Solution

(a) There are two loops in the circuit.

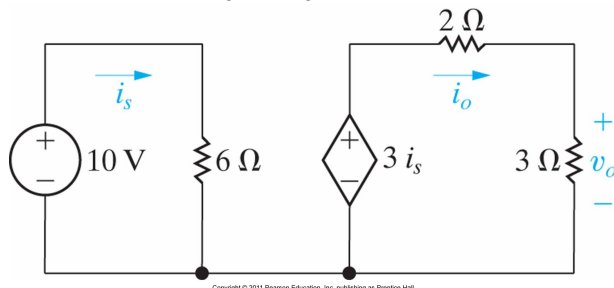
KVL for left part: $10 = 6i_s$

KVL for right part: $3i_s = 2i_o + 3i_o$

Solve two equations, we have:

$i_s = 1.67$ and $i_o = 1\text{A}$

Applying Ohm's Law, $v_o = 3 \cdot i_o = 3\text{V}$



Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

Solution

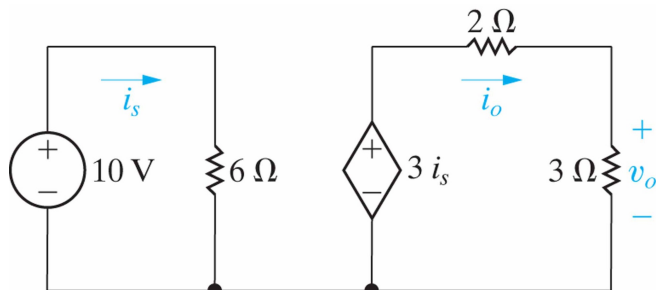
(b) $p_{10V} = -10 \cdot 1.67 = -16.7\text{W}$

$p_{ds} = 3i_s \cdot (-i_o) = -5\text{W}$

$p_6 = 1.67^2 \cdot 6 = 16.7\text{W}$, $p_2 = 1^2 \cdot 2 = 2\text{W}$, $p_3 = 1^2 \cdot 3 = 3\text{W}$

$P_{\text{total}} = -16.7 - 5 + 16.7 + 2 + 3 = 0\text{W}$

So the power is balanced.

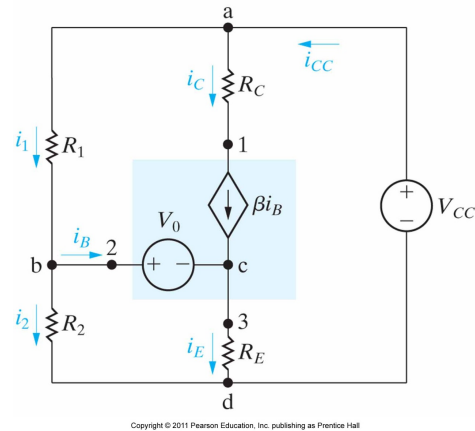


Copyright © 2011 Pearson Education, Inc. publishing as Prentice Hall

Activity 10

The circuit represents a common configuration encountered in the analysis and design of transistor amplifiers. Assume that the values of R_1 , R_2 , R_C , R_E , V_{CC} and V_0 are known.

Find i_B in terms of the circuit element values.



Solution

Apply KCL to nodes a, b, c, and 1, we have:

- (1) $i_1 + i_C - i_{CC} = 0$
- (2) $i_B + i_2 - i_1 = 0$
- (3) $i_E - i_B - i_C = 0$
- (4) $i_C = \beta i_B$

Apply KVL to 2 loops bcdb and badb, we have:

- (5) $V_0 + i_E R_E - i_2 R_2 = 0$
- (6) $-i_1 R_1 + V_{CC} - i_2 R_2 = 0$

Solution

Solve Eq.(6) for i_1 and substitute i_1 into Eq. (2)

$$i_1 = \frac{V_{CC} - i_2 R_2}{R_1}$$

$$\frac{V_{CC} - i_2 R_2}{R_1} = i_B + i_2 \Rightarrow i_2 = \frac{V_{CC} - i_B R_1}{R_1 + R_2}$$

Substitute i_2 to Eq.(5), solve for i_E

$$\frac{V_0 + i_E R_E}{R_2} = \frac{V_{CC} - i_B R_1}{R_1 + R_2} \Rightarrow i_E = \left(\frac{(V_{CC} - i_B R_1) R_2}{(R_1 + R_2) R_E} - \frac{V_0}{R_E} \right)$$

Solution

Substitute i_E into Eq. (3), and use Eq.(4) to eliminate i_C in Eq.(3), we have:

$$\frac{(V_{CC} - i_B R_1) R_2}{(R_1 + R_2) R_E} - \frac{V_0}{R_E} = i_B (1 + \beta)$$

$$\therefore i_B = \frac{V_{CC} R_2 / (R_1 + R_2) - V_0}{R_1 R_2 / (R_1 + R_2) + (1 + \beta) R_E}$$