

Paper survey

Here are some papers that you can consider for the paper review. If you have any other ideas, please discuss it with me.

Some of these papers have embedded links to the pdf file, some do not. You can use IEEEEXPLORE or google them.

1. S. Parameswaran , and T. Wolf, “Embedded systems security—an overview” . Des Autom Embed Syst (2008) 12: 173–183.
2. [Kornecki, A.J.](#); [Zalewski, J.](#); [Stevenson, W.F.](#) “Availability Assessment of Embedded Systems with Security Vulnerabilities” Software Engineering Workshop (SEW), 2011 34th IEEE
3. [Zafar, S.](#); [Dromey, R.G.](#) [Integrating safety and security requirements into design of an embedded system.](#) [Software Engineering Conference, 2005. APSEC '05. 12th Asia-Pacific](#)
4. [Jensen, M.](#); [Schwenk, J.](#); [Bohli, J.-M.](#); [Gruschka, N.](#); [Iacono, L.L.](#) [“Security Prospects through Cloud Computing by Adopting Multiple Clouds” Cloud Computing \(CLOUD\), 2011 IEEE International Conference on](#)
5. Raluca Ada Popa, Jay Lorch, David Molnar, Helen J. Wang, and Li Zhuang. [Enabling Security in Cloud Storage SLAs with CloudProof.](#) [BibTeX] USENIX 2011.
6. [Valamehr, J.](#) ; [Chase, M.](#) ; [Kamara, S.](#) ; [Putnam, A.](#) ; [Shumow, D.](#) ; [Vaikuntanathan, V.](#) ; [Sherwood, T.](#) “[Inspection resistant memory: Architectural support for security from physical examination](#)” ISCA 2012, Page(s):130 – 141
7. [Koka, P.](#) ; [McCracken, M.O.](#) ; [Schwetman, H.](#) ; [Chen, C.-H.O.](#) ; [Xuezhe Zheng](#) ; [Ho, R.](#) ; [Raj, K.](#) ; [Krishnamoorthy, A.V.](#) “[A micro-architectural analysis of switched photonic multi-chip interconnects](#)” ISCA 2012, Page(s):153 - 164
8. Kshitij Sudan, Saisanthosh Balakrishnan, Sean Lie, Min Xu, Dhiraj Mallick, Gary Lauterbach, Rajeev Balasubramonian “A Novel System Architecture for Web Scale Applications Using Lightweight CPUs and Virtualized I/O” HPCA 2013
9. [Efficiently Prefetching Complex Address Patterns.](#) Manjunath Shevgoor, Sahil Koladiya, Rajeev Balasubramonian, Seth Pugsley, Chris Wilkerson, Zeshan

Chishti, *48th International Symposium on Microarchitecture (MICRO-48)*, Hawaii, December 2015.

10. D. Joseph and D. Grunwald, "Prefetching Using Markov Predictors," in *Proceedings of ISCA*, 1997.
11. Hur and C. Lin, "Memory Prefetching Using Adaptive Stream Detection", in *Proceedings of MICRO*, 2006.
12. Yunsup Lee, Vinod Grover, Ronny Krashinsky, Mark Stephenson, Stephen W. Keckler, and Krste Asanović, "Exploring the Design Space of SPMD Divergence Management on Data-Parallel Architectures", *47th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO-47)*, Cambridge, UK, December 2014. [PDF](#)
13. B. Zhang, N. Mor, J. Kolb, D. S. Chan, N. Goyal, K. Lutz, E. Allman, J. Wawrzynek, E. A. Lee, and J. D. Kubiatowicz, "[The Cloud is Not Enough: Saving IoT from the Cloud](#)," in *Proceedings of the 7th USENIX Conference on Hot Topics in Cloud Computing*, HotCloud'15, Berkeley, CA, USA: USENIX Association, 2015, pp. 21--21.
14. [ISCA] "Towards Energy Proportionality for Large-Scale Latency-Critical Workloads," David Lo, Liqun Cheng, Rama Govindaraju, Luiz Barroso, Christos Kozyrakis *Proceedings of the 41st Intl. Symposium on Computer Architecture (ISCA)*, Minneapolis, MN, June 2014. [\[PDF\]](#) [\[slides\]](#)