Dept. of Computer Science and Engineering
CSE3201 – Digital Logic Design

Lab 1

Objectives:

To use the DE2 board to design a medium complexity system.

Lab Description

Design and implement a stop watch on the DE2 board.
The watch is controlled by the pushbutton switches on the board
SW0: resets the clock to 0
SW1: start and restart counting and displaying on the 7-segment display
SW2: Stops counting

Deliverables
1. You have to show to the TA the circuit working on the board.
2. A report showing the high level design, and the V4rilog code (and pin assignment)

The Demo is on the second week of labs.
The report is due the same time as the demo.