Small Loop Antenna Calculations

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Abstract—A somewhat prolonged discussion of the performance of small loop antennas operating in free-space.

I. INTRODUCTION

A previous technical note [1] presented a number of closed form expressions for a loop antenna in free-space. Here we generate some numbers to get an impression of this antenna’s performance. Specifically, we are interested in very small antennas on the order of several millimeters. The kind that in one form or another can be wrapped around (or deposited on) a chip as illustrated in Fig. 1.

Fig. 1. A rectangular loop antenna wound around the periphery of a chip.

II. CALCULATION

Matlab is used to calculate all the key antenna performance parameters. The first order of business is to calculate the radiation resistance $R_r$. For a loop antenna the radiation intensity (in Watts per steradian) is [1]

$$U(\theta, \phi) = \frac{1}{2} |E(\theta, \phi) \times \mathbf{H}^* (\theta, \phi)|$$

$$= \frac{2I^2}{\pi^2} \sqrt{\mu_0 \varepsilon_0} \frac{\sin^2 \left( \frac{\pi a}{\lambda} \sin \theta \cos \phi \right) \sin^2 \left( \frac{\pi a}{\lambda} \sin \theta \sin \phi \right)}{\sin \theta \sin^3 2\phi}$$

for a square loop $a$ meters on a side is considered.

The radiation resistance can be calculated with

$$R_r = \frac{2}{T_{00}} \int_{\Omega} U(\theta, \phi) d\Omega$$

$$= \frac{2}{T_{00}} \int_{0}^{2\pi} d\phi \int_{0}^{\pi} U(\theta, \phi) \sin \theta d\theta.$$

The strip of Matlab code used to actually calculate $R_r$ is

$$R_{temp} = \text{dblquad}(\text{Rrdiff2}, 1e-10, 2\pi, 1e-10, \text{pi}, 1e-10, [], \text{len}, \text{lam});$$

$$R_r(\text{row}, \text{col}) = R_{temp};$$

$$R_r = 4/\pi^2 \sqrt{\mu_0 \varepsilon_0} R_r;$$

where the Rrdiff2 function called by the dblquad integration command is

$$\text{function z} = \text{Rrdiff2}(\text{phi, theta, lenx, lamx});$$

$$\text{z} = (\sin(\pi \text{lenx}/\text{lamx} \sin(\text{theta}) \cos(\text{phi}))) \cdot 2 \cdot (\sin(\pi \text{lenx}/\text{lamx} \sin(\text{theta}) \sin(\text{phi}))) \cdot 2 \cdot (\sin(\text{theta})) \cdot (\sin(2 \cdot \text{phi})) \cdot (-1) \cdot (\sin(2 \cdot \text{phi})) \cdot (-2);$$

As per the discussion in [1] and [2] we also calculate the (maximum) gain, $G$, and the antenna efficiency, $\eta$, by calculating the resistive (thermal) loss, $R_l$. For the purpose of receiver and transmitter design, it is also important to know the self-inductance of the antenna. The equation for this was presented in [1].

III. RESULTS

A natural, lumped element model of the antenna consists of an inductor in series with a resistor as shown shown in Fig. 2. The series inductance can be substantial even for the chip-scale dimensions under consideration (e.g. $\sim 10$ nH). Compact expression for the loop inductance, $L$, and ohmic losses, $R_l$ are given in [1].

$$R_a = R_r + R_l \ L$$

Fig. 2. Antenna lumped equivalent circuit.

The width, $w$, of the metal antenna trace possibly ranges from 2 $\mu$m to 500 $\mu$m (which would be rather wide considering typical chip dimensions). The conductivity, $\sigma$, of the trace obviously depends on the metal that it is made out of (3.77 x 10^7 [Ω·m]^{-1} for Al and 5.96 x 10^7 [Ω·m]^{-1} for Cu). We can expected the thickness of the loop’s metal trace, $t$, to vary between 1 $\mu$m and 5 $\mu$m.
An earlier technical report [1] highlighted the proportion-
alities $R_e \propto \omega^4$ and $R_t \propto \sqrt{2}$. This is conveyed in the 3D efficiency plot shown in Fig. 3 as a function of the antenna’s dimensions (a single square loop is simulated) and operational frequency. This result assumes a copper antenna trace that is 400-μm wide and 3-μm thick.

On the whole the results are quite promising, even at a 2-GHz carrier we can achieve a 9.1% efficiency for a 6-mm square loop antenna. At 6 GHz this improves to 83%. Even a compact 3-mm on-a-side loop operates with an efficiency of 39% at this frequency.

To gain some perspective on the trade-offs, a plot of the antenna dimensions (assuming copper traces) needed to achieve 50% and 10% efficiencies at various frequencies of operation are shown in Fig. 4.

As indicated by these plots a 50% requires a rather manageable footprint with a 4 mm × 4 mm 400-μm wide copper structure excited at 5 GHz. If we can settle for a 10% efficiency the antenna area is quartered to a 2 mm × 2 mm (still 400-μm wide) running at 5 GHz.

For the purpose of circuit design it is worthwhile to examine the total resistance of the antenna. This indicates the driving point impedance that the receiver has to present for optimum power transfer. Fig. 5 shows the total series resistance ($R_t = R_e + R_w$) of the copper antenna under consideration ($w = 400 \mu m$, $t = 3 \mu m$) at various sizes and frequencies of operation. Beside each resistance data point is indicated the efficiency of the antenna. In the options shown, the antenna resistance ranges from 0.3 Ω to 50 Ω. Within the rectangular region denoting an acceptable range of frequency operation and antenna size the antenna resistance varies from 0.35 Ω to 10 Ω. Practically speaking, it is hard to imagine that any structure with $R_t < 1 \Omega$ is worth considering, as the losses incurred in the interface to the receiver should far outweigh this therefore completely blotting out the ideal efficiency values quoted.

Even in the 1 Ω to 10 Ω range the designer will have to exact considerable care in designing the interface. Still, when approaching this problem from a purely integrated viewpoint this challenge becomes somewhat less daunting. If confined to the 50-Ω RF system paradigm, the designer is indeed challenged with the implementation of a matching network that converts the low series resistance (on the order of 5 Ω as seen in Fig. 5) of the loop without introducing so much loss that the already strained antenna efficiency is even further compromised. Luckily, when considering a custom integrated circuit design, we have a good amount of freedom to determine our terminating impedances, thus giving hope to the usefulness of low-resistance antenna designs.

Obviously, a great many options are available to the prospective designer. Table I summarizes some possibilities with acceptable sizes and operating frequencies. The table includes measures of the antenna quality factor

$$Q = \frac{\omega_0 L}{R_a}$$

and the antenna’s bandwidth in Hz

$$BW = \frac{f_0}{Q}.$$ 

![Efficiency of square copper loop antenna](image)

![Net resistance of square copper loop antenna](image)

**Fig. 3.** Efficiency of square copper loop antenna ($w = 400 \mu m$ and $t = 3 \mu m$) as a function of its dimensions and frequency of operation.

**Fig. 5.** Net resistance of square copper loop antenna ($w = 400 \mu m$ and $t = 3 \mu m$) as a function of its dimensions and frequency of operation.

**TABLE I**

<table>
<thead>
<tr>
<th>$l_x = l_y$</th>
<th>$f_0$ [GHz]</th>
<th>$\eta$ [%]</th>
<th>$Q$</th>
<th>$BW$ [MHz]</th>
<th>$L$ [nH]</th>
<th>$R_e + R_t$ [Ω]</th>
<th>$G$ [dB]</th>
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<tbody>
<tr>
<td>2</td>
<td>2.50</td>
<td>0.76</td>
<td>300</td>
<td>8.32</td>
<td>5.51</td>
<td>0.29</td>
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<td>12.5</td>
<td>422</td>
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<td>0.45</td>
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<td>307</td>
<td>32.6</td>
<td>5.51</td>
<td>1.13</td>
<td>-0.93</td>
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<td>2.50</td>
<td>6.29</td>
<td>341</td>
<td>7.33</td>
<td>5.51</td>
<td>0.61</td>
<td>-10.3</td>
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<td>53.0</td>
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<td>90.0</td>
<td>80.0</td>
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<td>10.4</td>
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<td>6</td>
<td>2.50</td>
<td>18.4</td>
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Now, we come across the limitation that without a matching network placed between the antenna and driving circuit is bound to introduce more ohmic losses, thus further lowering the efficiency. This problem can be sidestepped by designing the transceiver circuit to interface directly to the antenna, however in this case, it will be difficult to couple a significant amount of power into the antenna — not an unreasonable limitation in purported low-power systems.

To illustrate, we refer to the equivalent antenna circuit in Fig. 6 where we reconsider the general model of Fig. 2 in transmit mode (therefore dropping \( v_S \)) as a parallel equivalent circuit with \( C \) acting as some tuning capacitance. As shown

\[
R_p = Q^2 R_a
\]

(5)

where \( Q \) is the antenna’s quality factor.

Assuming a sinusoidal voltage excitation with amplitude \( V_0 \) across this circuit the radiated power is

\[
P_{rad} = \frac{\eta V_0^2}{2R_p}.
\]

(6)

Now, we come across the limitation that without a matching network, \( V_0 \) is roughly limited to the DC supply level, \( V_{DD} \). Assuming a \( V_{DD} = 2 \) V the possible power radiated from the antennas (only 5.5-GHz frequencies are considered) is given in Table II. Also shown is the power that must be injected into the antenna (presumably from the DC supply) in order to sustain the radiation. If we imagine that a DC current \( I_{dc} \) is (sinusoidally) switched back and forth across the resistor then the relationship between \( I_{dc} \) and \( V_0 \) is simply

\[
I_{dc} = \frac{V_0}{R_p}.
\]

(7)

The limited supply and relatively poor radiative characteristics of the antenna combine to necessitate the very low dc currents listed in Table II.

A drawing of an antenna driver in the form of an active antenna connection is shown in Fig. 7. With ideal switches, the efficiency of this amplifier is 100%. Unfortunately, this property is deceiving as even an extremely good (but lossy) switch in the circuit will immediately reduce the efficiency of this circuit to 50%. Thus, no currently conceivable technology can hope to improve the efficiency of this circuit beyond 50%.

As shown in Table I the maximum gain of the antennas under consideration falls to the 6 mm × 6 mm model and is only 0.7 dB. The distribution of this antenna’s gain parameter in space is shown in Fig. 8.

Assuming maximum practical injected power (Table II) the available power of the receiving antenna as well as the peak voltage and current values from the Thevenin and Norton antenna equivalent models (see Fig. 9) are tabulated in Table III as a function of the separation distance, \( r \), (between transmitter and receiver) where the peak voltage and current values are calculated according to

\[
V_{pk} = \sqrt{8 \cdot P_R \cdot R_a}
\]

(8)
Fig. 7. An idealized schematic of an active antenna.

Fig. 8. The gain distribution (maximum of 0.7 dB) of the 6 mm loop antenna at 5.5 GHz.

\[ I_{pk} = \sqrt{8 \cdot P_R / R_p} \]  
\[ P_R = \frac{\lambda^2 G_T G_R P_{\text{inj}}}{(4\pi r)^2} \]  
\[ I_{pk} = \sqrt{8 \cdot \frac{\lambda^2 G_T G_R}{(4\pi r)^2} \cdot \frac{I_{dc}^2 R_p}{2} \cdot \frac{1}{R_p}} \]

TABLE III
6-mm RECEIVE ANTENNA SIGNAL STRENGTHS

<table>
<thead>
<tr>
<th>r [m]</th>
<th>( P_R ) [pW]</th>
<th>( P_R ) [dBm]</th>
<th>( V_{pk} ) [( \mu )V]</th>
<th>( I_{pk} ) [nA]</th>
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<tr>
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<td>205</td>
<td>-76.9</td>
<td>301</td>
<td>40.2</td>
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<td>10</td>
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